

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	1302	((message massaging vibrate vibrating) with (time duration)) and (controller microcontroller microprocessor processor computer) and (ram rom)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/12 07:36
L3	726	2 and @ad<="20030422"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/12 07:36
L4	413	((message massaging vibrate vibrating) with (time duration)) and (controller microcontroller microprocessor processor computer) and (ram and rom) and @ad<="20030422"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/12 07:38
L5	4	((message massaging vibrate vibrating) with (time duration)) and (controller microcontroller microprocessor processor computer) and (ram and rom) and (tanizawa miyaguchi nishio tsukada nishibori muto).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/12 07:54
L6	12	((message massaging vibrate vibrating) and (controller microcontroller microprocessor processor computer)).clm. and (tanizawa miyaguchi nishio tsukada nishibori muto).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/12 07:55
S1	2547	((message massaging vibrate vibrating) with (time duration)) and (controller microcontroller microprocessor processor computer) and ((memory with table) memory ram rom)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 09:48

S2	1441	S1 and @ad<="20030422"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 09:50
S3	64	((((message massaging vibrate vibrating) with (time duration)) and (controller microcontroller microprocessor processor computer) and ((memory with table) memory ram rom)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 10:14
S4	39	S3 and @ad<="20030422"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 10:15
S5	111	((((message massaging vibrate vibrating) with (time duration)) same (controller microcontroller microprocessor processor computer) same ((memory with table) memory ram rom))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 10:25
S6	72	S5 and @ad<="20030422"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 10:26
S7	361	((((message massaging vibrate vibrating) same (time duration)) same (controller microcontroller microprocessor processor computer) same ((memory with table) memory ram rom))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 10:29
S8	197	S7 and @ad<="20030422"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 10:29

S9	719	S2 and ((time duration) with predetermined)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 10:37
S10	39	S9 and S2 and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 11:05
S11	4157	((601/15) or (601/46) or (601/49) or (601/51) or (601/52) or (601/56-60) or (601/70) or (601/84) or (601/86) or (601/90) or (601/98) or (601/99) or (601/100) or (601/102) or (601/103) or (601/115) or (601/116) or (600/26) or (600/27) or (600/28) or (128/905) or (601/87) or (601/93) or (601/94) or (601/101)).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/11 11:09
S12	1943	S11 and @ad<="20030422"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 11:09
S13	37	S12 and (range with (height width))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 11:14
S14	38	S12 and S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 11:21
S15	36	S14 not S13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 11:21

S16	17	S15 and ((time duration) with predetermined)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 11:22
S17	15	S16 not S10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/11 11:22

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